



**EXPEDITED PROCEDURE --EXAMINING GROUP 2116**

**S/N 09/709,001**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Matthew B. Haycock <i>et al.</i>	Examiner:	Eric Chang
Serial No.:	09/709,001	Group Art Unit:	2116
Filed:	November 8, 2000	Docket No.:	884.302US1
Title:	DIGITAL BUS		
Assignee:	Intel Corporation	Customer No:	21186

**AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.116**

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**OCT 18 2004**

**Technology Center 2100**

This paper is filed in response to the final Office action mailed on August 11, 2004.

Please amend the above-identified patent application as follows:

OK to ENTER

1/21/05

EC